

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Atty Docket: 02-GR2-463
Patrick PERILLAT : APPLICATIONS BRANCH
Serial No. (not yet assigned) :
Filed: HEREWITH :

17548 U.S.PTO
10/781557



02804

For: **SEMICONDUCTOR PACKAGE CONTAINING AN INTEGRATED-CIRCUIT CHIP
SUPPORTED BY ELECTRICAL CONNECTION LEADS**

CERTIFICATE OF EXPRESS MAIL MAILING

"Express Mail" Mailing Label No. **EV381145765US**
Date of Deposit: **February 18, 2004**

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SIR:

I hereby certify that

Application Transmittal
 Fee Transmittal Form
 Specification, Claims, Abstract
 1 set of 6 sheets of drawings
 Unexecuted Declaration and Power of Attorney
 Information Disclosure Statement and Form PTO-1449 w/ 2 non-U.S. patent
references
 Return postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to:

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

2/18/04
Date of Deposit

Kathleen Schlochon
Name of person mailing papers

Kathleen Schlochon
Signature

**UTILITY PATENT APPLICATION TRANSMITTAL**

ADDRESS TO: Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith for filing is the patent application of:

INVENTOR(S): Patrick PERILLAT

TITLE: SEMICONDUCTOR PACKAGE CONTAINING AN INTEGRATED-CIRCUIT CHIP
SUPPORTED BY ELECTRICAL CONNECTION LEADS

ASSIGNEE INFORMATION PURSUANT TO 37 C.F.R. 1.76

ASSIGNEE NAME: STMICROELECTRONICS SA
ASSIGNEE RESIDENCE: MONTROUGE, FRANCE

In connection with this application, the following are enclosed:

Fee Transmittal Form
 15 Pages of Specification, Claims and Abstract
 20 Claims
 6 Sheets of Drawings (FIGS. 1-7)
 Unexecuted Declaration and Power of Attorney
 Information Disclosure Statement and Form PTO-1449 w/ 2 non-U.S. patent references
 Please direct all correspondence to **Customer Number 23334.**

Respectfully submitted,

FLEIT, KAIN, GIBBONS, GUTMAN,
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FEE TRANSMITTAL

Transmitted herewith for filing is the patent application of:

INVENTOR(S): Patrick PERILLAT

TITLE: SEMICONDUCTOR PACKAGE CONTAINING AN INTEGRATED-CIRCUIT CHIP
SUPPORTED BY ELECTRICAL CONNECTION LEADS

The fee has been calculated as shown below. (Small entity fees indicated in parentheses.)

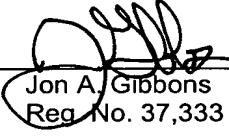
For	Number Filed		Number Extra	Rate Large (Small)	Basic Fee \$770 (\$385)
Total Claims	20	20	0	\$18 (\$9)	0
Independent Claims	1	3	0	\$86 (\$43)	0
Multiple Dependent Claims				\$290 (\$145)	0
Assignment Recording Fee				\$40	0
					TOTAL FEE: \$ 770

The Commissioner is hereby authorized to charge Deposit Account No. _____ in the amount of \$ _____.

The Commissioner is hereby authorized to charge payments of (1) any additional filing fees required under 37 CFR 1.16, and/or (2) any patent application processing fees under 37 CFR 1.17 associated with this application or credit any overpayment to Deposit Account No. _____.

Respectfully submitted,

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